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Dated: November 17, 2005 Signature: Valerie Cohen  
(Valerie Cohen)

Docket No.: 495152000402  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Hui WANG et al.

Application No.: 10/721,391

Confirmation No.: 1659

Filed: November 24, 2003

Art Unit: 1753

For: METHOD AND APPARATUS FOR HOLDING  
AND POSITIONING  
SEMICONDUCTOR WORKPIECES DURING  
ELECTROPOLISHING AND/OR  
ELECTROPLATING OF THE WORKPIECES

Examiner: Not Yet Assigned

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in

Under "Domestic Priority data as claimed by applicant," please change "60/110,136" to  
- -60/110,134 - - .

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of

sf-2036909

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such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing docket no. 495152000402. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: November 17, 2005

Respectfully submitted,

By

Peter J. Yim

Registration No.: 44,417  
MORRISON & FOERSTER LLP  
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(415) 268-6373

sf-2036909



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Washington, D.C. 20231-1450  
[www.uspto.gov](http://www.uspto.gov)

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/721,391	11/24/2003	1753	421	495152000402	55	24	3

CONFIRMATION NO. 1659

20872  
 MORRISON & FOERSTER LLP  
 425 MARKET STREET  
 SAN FRANCISCO, CA 94105-2482

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MORRISON &amp; FOERSTER

FILING RECEIPT



\*OC000000011985551\*

Date Mailed: 02/27/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Hui Wang, Fremont, CA;  
 Felix Gutman, San Jose, CA;  
 Voha Nuch, San Jose, CA;

**Domestic Priority data as claimed by applicant**

This application is a CON of 09/856,855 05/23/2001 \*  
 which is a 371 of PCT/US99/28106 11/24/1999  
 which claims benefit of 60/110,134 11/28/1998  
 (\*)Data provided by applicant is not consistent with PTO records.

60/110,134 11/28/1998

**Foreign Applications****If Required, Foreign Filing License Granted:** 02/26/2004**Projected Publication Date:** 06/03/2004**Non-Publication Request:** No**Early Publication Request:** No**\*\* SMALL ENTITY \*\*****Title****DOCKETED**

Method and apparatus for holding and positioning semiconductor workpieces during electropolishing and/or electroplating of the workpieces

Preliminary Class

205

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15

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